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		APPLICABLE DIVISION
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	<h2>SPECIFICATION</h2>	

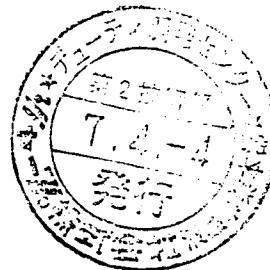
SPECIFICATION FOR  
Passive Matrix LCD Module

Model No.  
**LM32K071**

CUSTOMER'S APPROVAL

DATE \_\_\_\_\_

BY \_\_\_\_\_



PRESENTED  
BY Y. Inoue

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**1. Application**

This data sheet is to introduce the specification of LM32K071, Passive Matrix type LCD module.

**2. Construction and Outline**

Construction: 320×240 dots graphic display module consisting of a LCD panel, PWB(printed wiring board) with electric components mounted onto, TAB(tape automated bonding) to connect the LCD panel and PWB electrically, and plastic chassis with CCFT back light and bezel to fix them mechanically.

Outline : See Fig. 7

Connection : See Fig. 7 and Table 6.

**3. Mechanical Specifications**Table 1

Parameter	Specifications	Unit
Outline dimensions Note 1)	170 (W) × 110 (H) × 8 MAX (D)	mm
Viewing area	121 (W) × 91.6 (H)	mm
Active area	115.17 (W) × 86.37 (H)	mm
Display format	320 (W) × 240 (H)	-
Dot size	0.33 (W) × 0.33 (H)	mm
Dot spacing	0.03	mm
Base color Note 2)	Normally white Note 3)	-
Mass	Approx 220	g

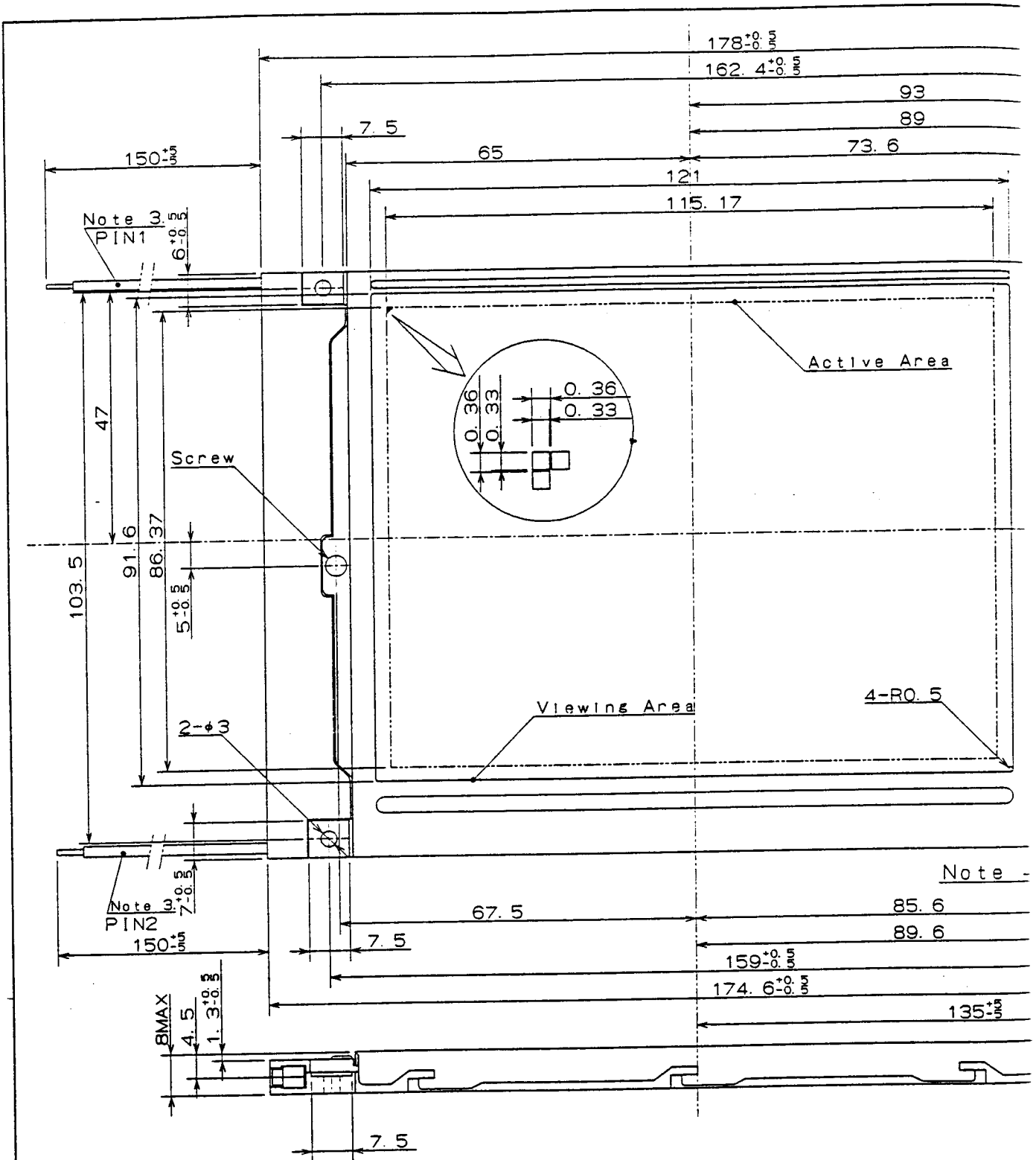
Note 1) Excluded the mounting portions and connectors.

Note 2) Due to the characteristics of the LC material, the colors vary with environmental temperature.

Note 3) Positive-type display

Display data "H" : ON → Black

Display data "L" : OFF → White



PCB to Flat cable.

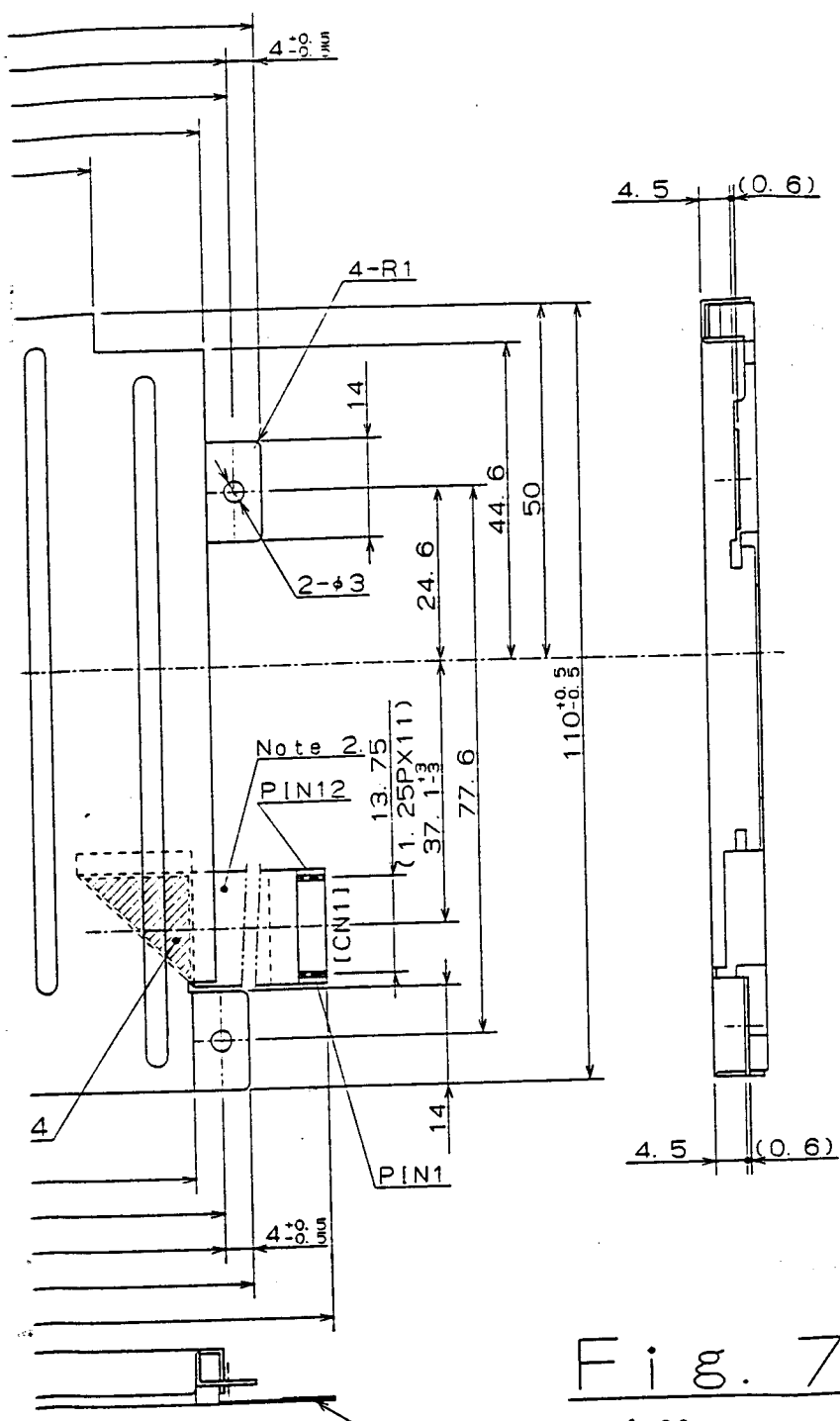
4. Both faces adhesion tape is between

3. backlight cable:UL3239. AWG22

2. Flat cable (TW-VF 1.25P FUJIKURA)

Note. 1. Bezel is plated by white-Zinc.

出 ISSUE	
設計 DRAWING INFO	規格 INFORMATION
) 記号	
新 NEW	変 CHANGE
替 REPLACE	図 FIGURE



INTERFACE SYMBOL

Pin No	SINBOL	Pin No	SINBOL
1	V <sub>0</sub>	7	NC
2	VEE	8	VSS
3	D3	9	VDD
4	D2	10	CP2
5	D1	11	CP1
6	D0	12	S

Fig. 7

指示なき寸法公差は ±0.3 とする  
UNSPECIFIED TOL TO BE ±0.3

Electrode surface

LM32K071					名 称 NAME		LCD MODULE OUTLINE DIMENSIONS	
年月日 DATE	改定記事 REVISE	製造 No PREPA	担当 MODEL	記 号 SYMBOL		部品コード PARTS CODE		
材 質 MATERIAL	厚 薄 THICKNESS	仕 上 FINISH	尺 度 SCALE	1/1				
SHARP CORPORATION					作成日付 DATE		1995.3.29	
シャープ株式会社 液晶(専本)					図 番 DRAWING No			
発行部門 L. C. D Group								
設計 DESIGN	描図 TRACE	検図 CHECK	検図 CHECK	承認 APPROVE				
I Harashi		T Murota	M. Kikuchi		I MITSUMOTO			

## 5-2 Interface signals

Table6-1 CN1

Pin No	Symbol	Description	Level
1	V0	LCD contrast adjust voltage	-
2	VEE	Power supply for LCD	-
3	D3	Display data signal	-
4	D2		H(ON),L(OFF)
5	D1		
6	D0		
7	NC	-	
8	VSS	Signal ground	-
9	VDD	Power supply for Logic	-
10	CP2	Data input clock signal	"H"→"L"
11	CP1	Input data latch signal	"H"→"L"
12	S	Scan start-up signal	"H"

Used Cable : 1.25 mm pitch, 12 pins F.F.C

Table 6-2 CN2

Pin No	Symbol	Description
1	VFT1	Power supply for CCFT back light
2	VFT2	Power supply for CCFT back light

Pin No. and its location are shown in fig. 7.